

Figure 1

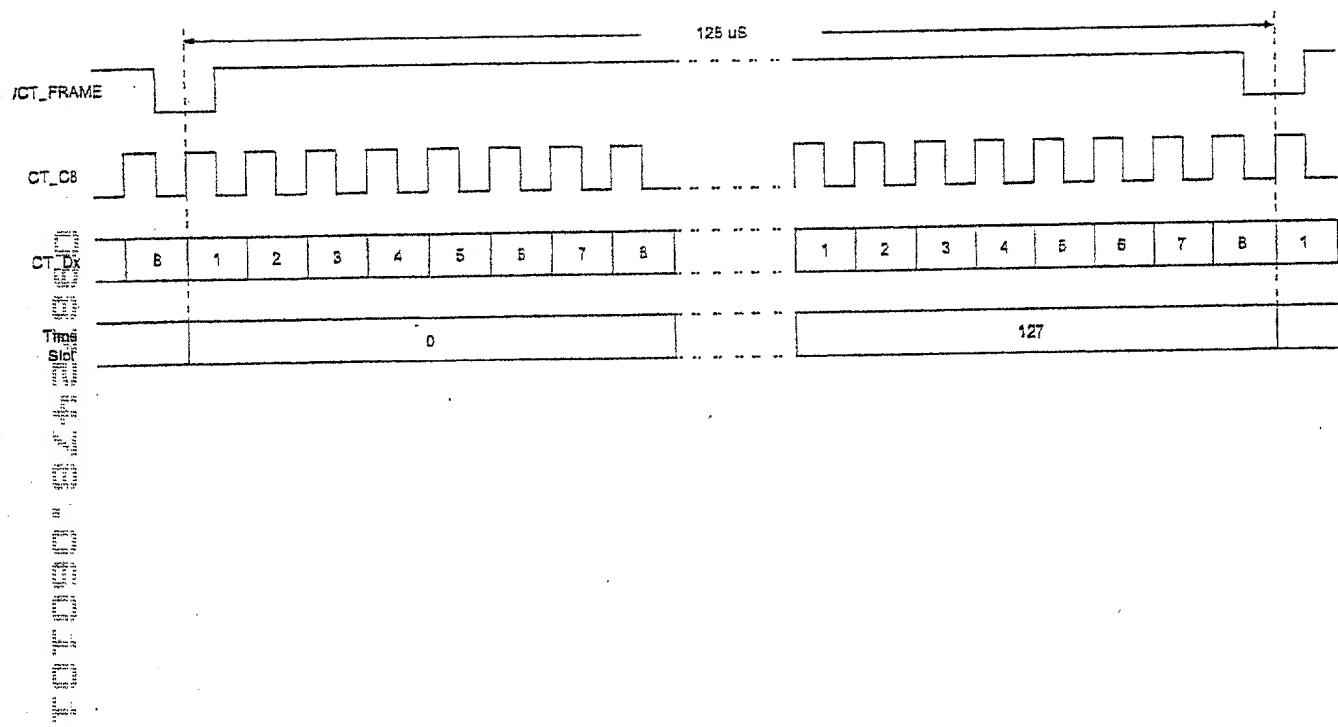


Figure 2

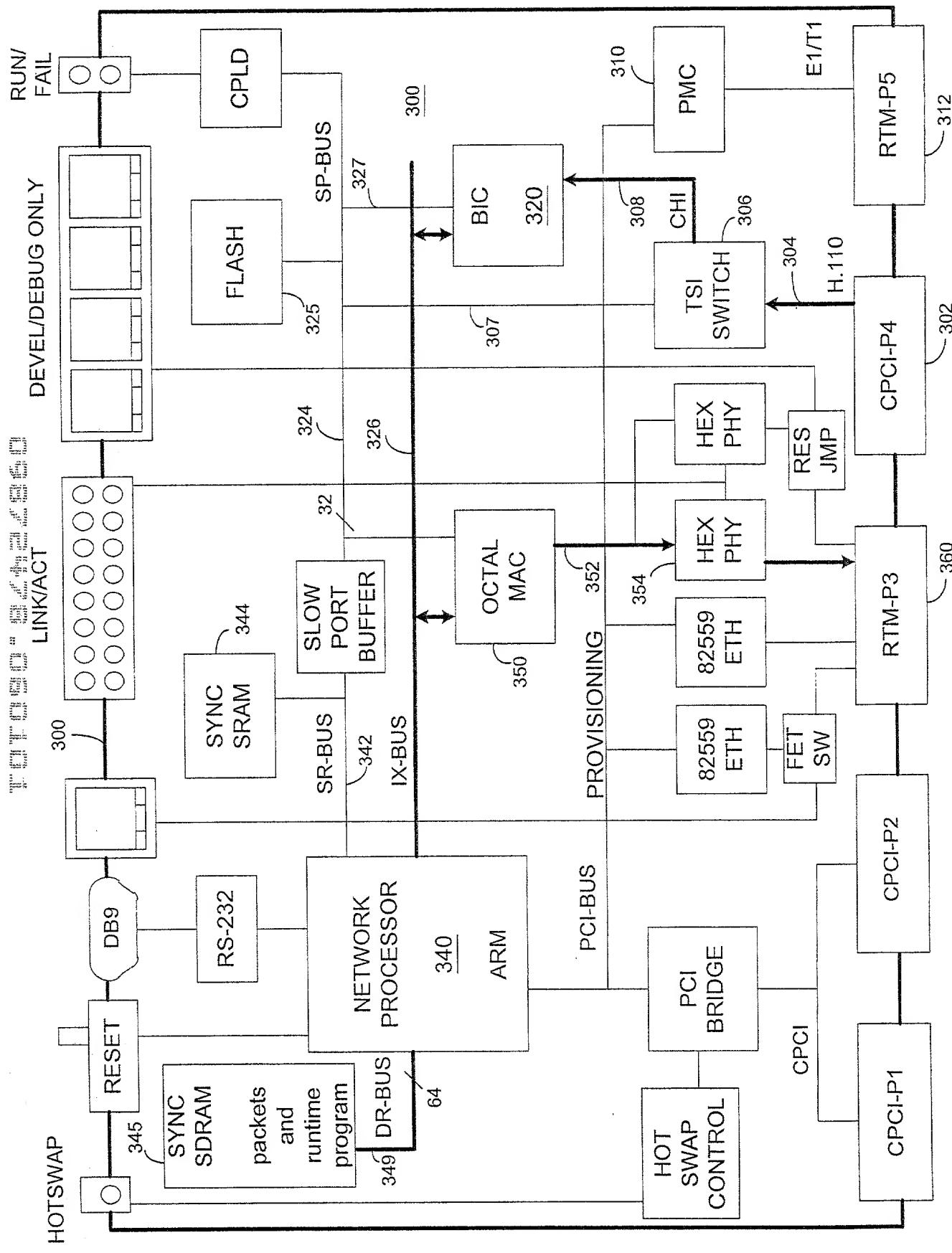


Figure 3

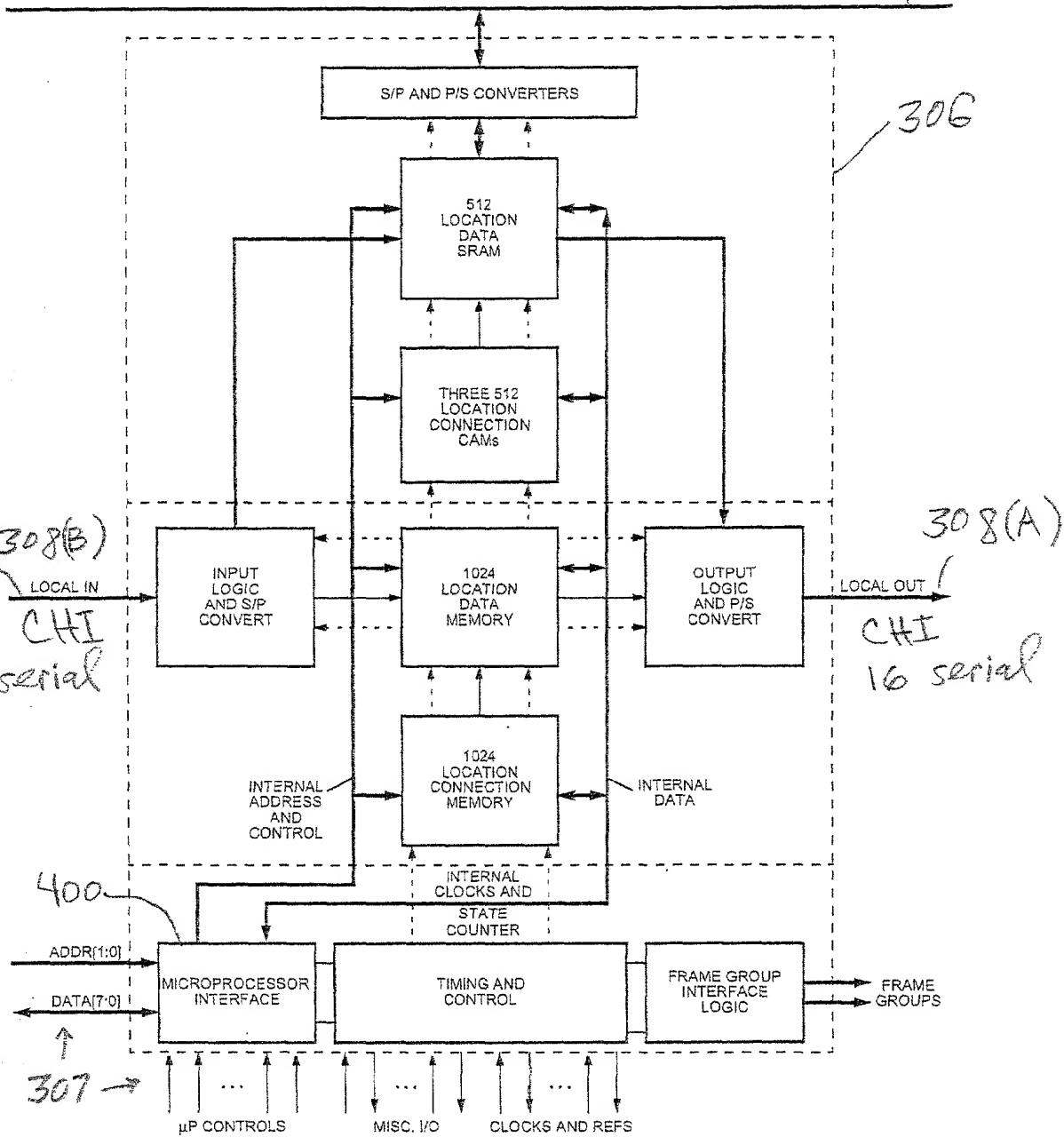


Figure 4

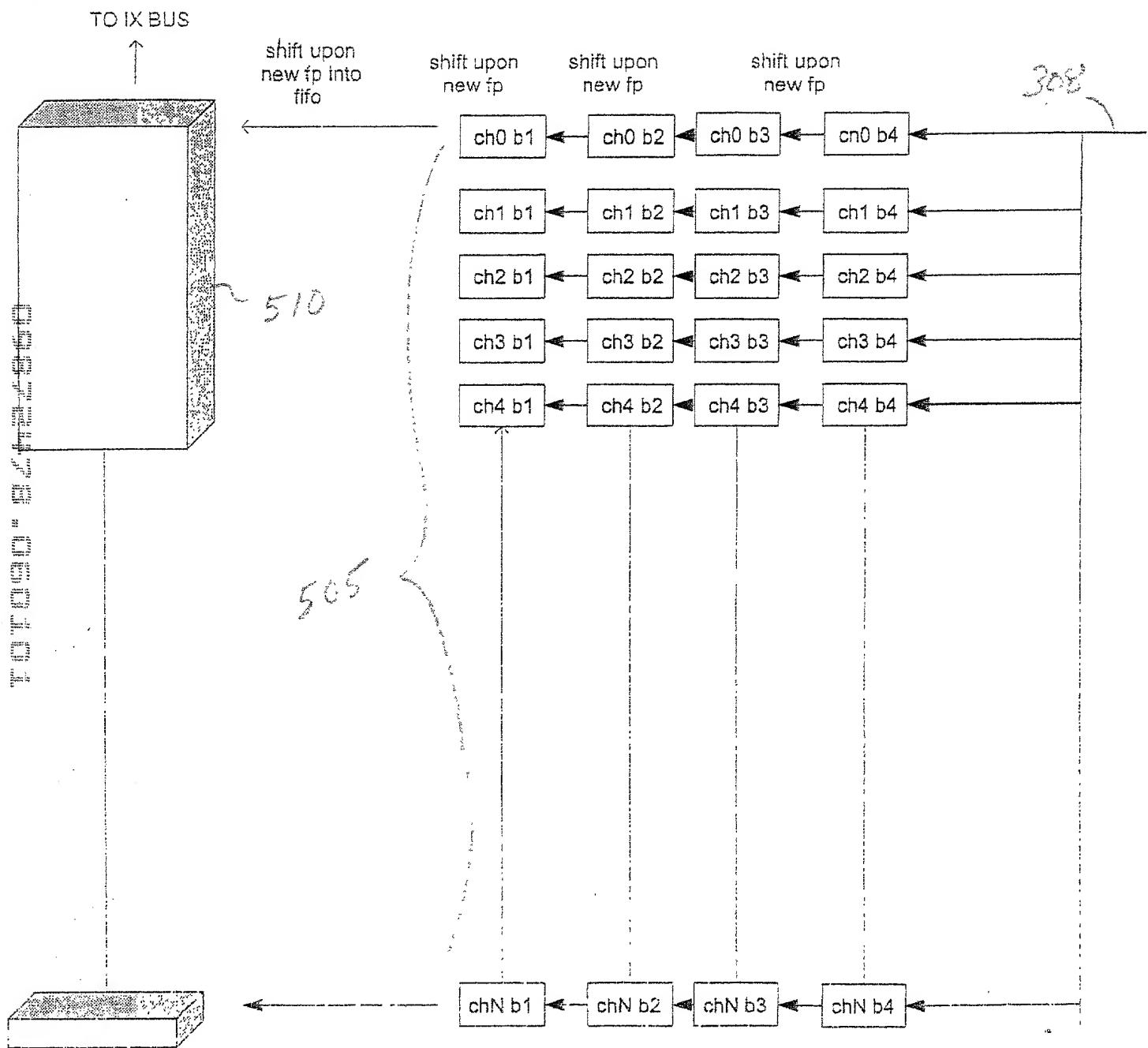


Figure 5

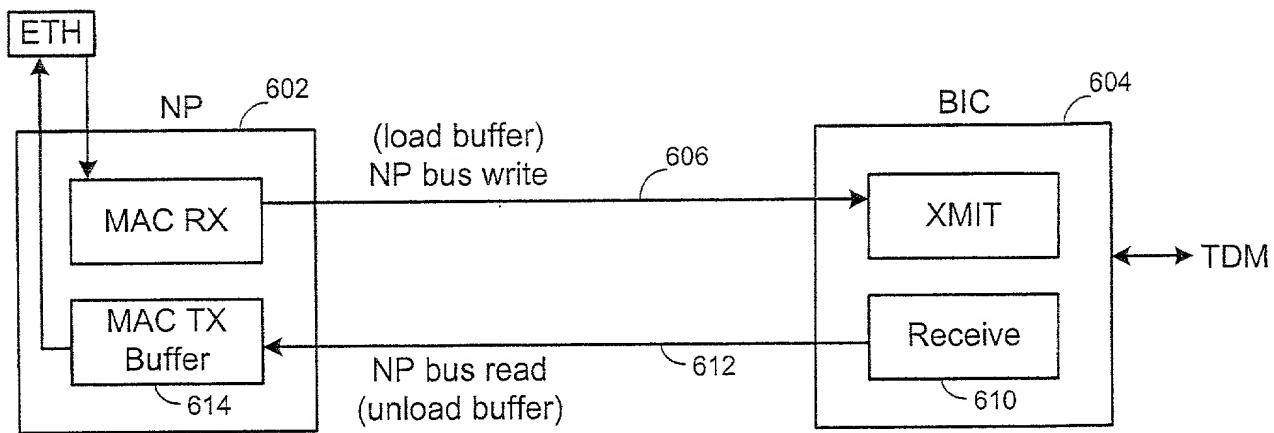


Figure 6A

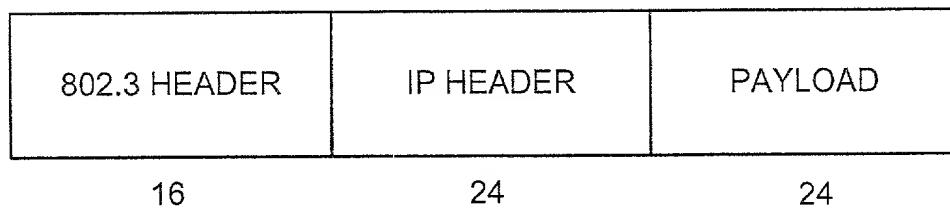


Figure 6B

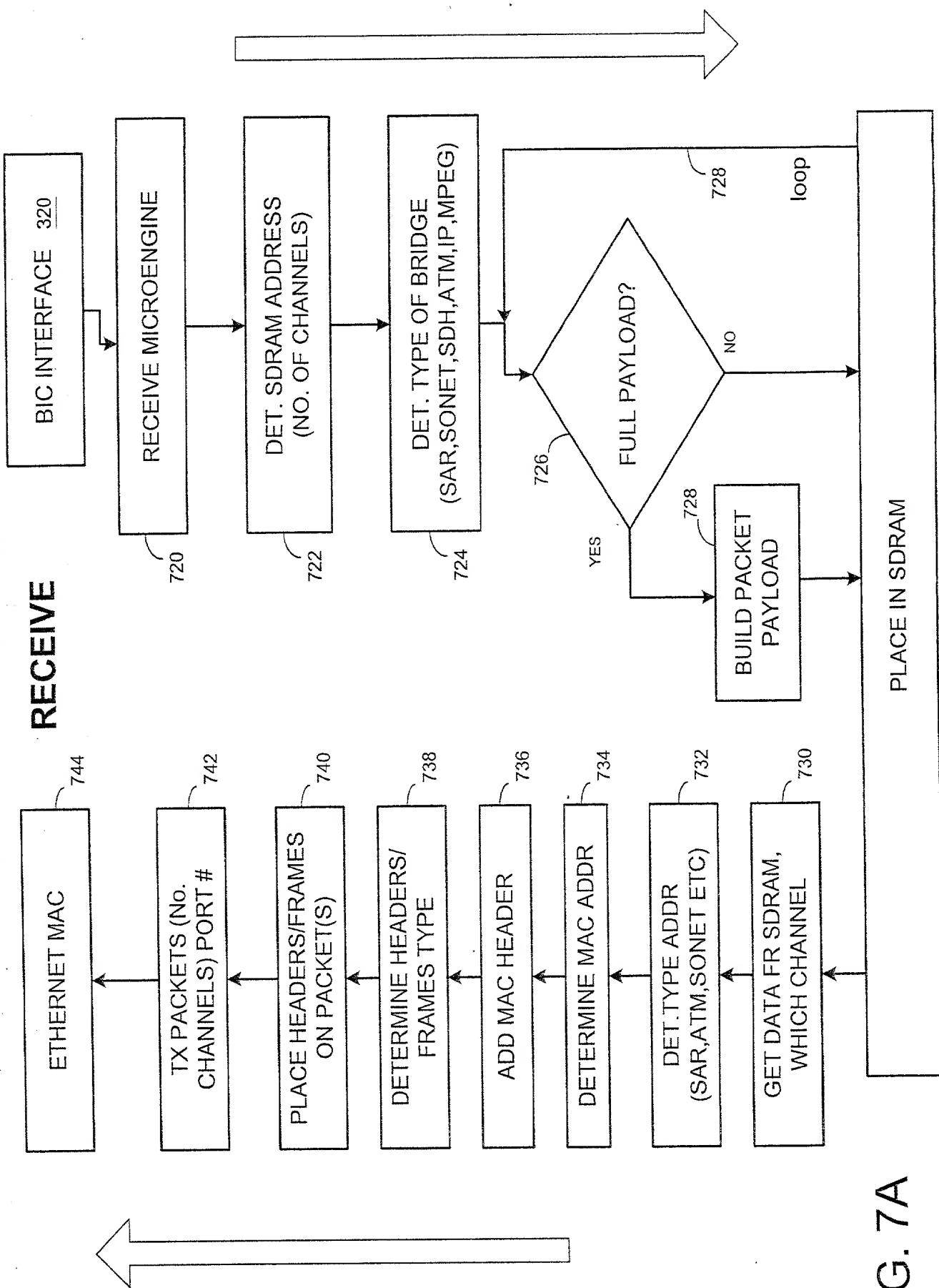


FIG. 7A

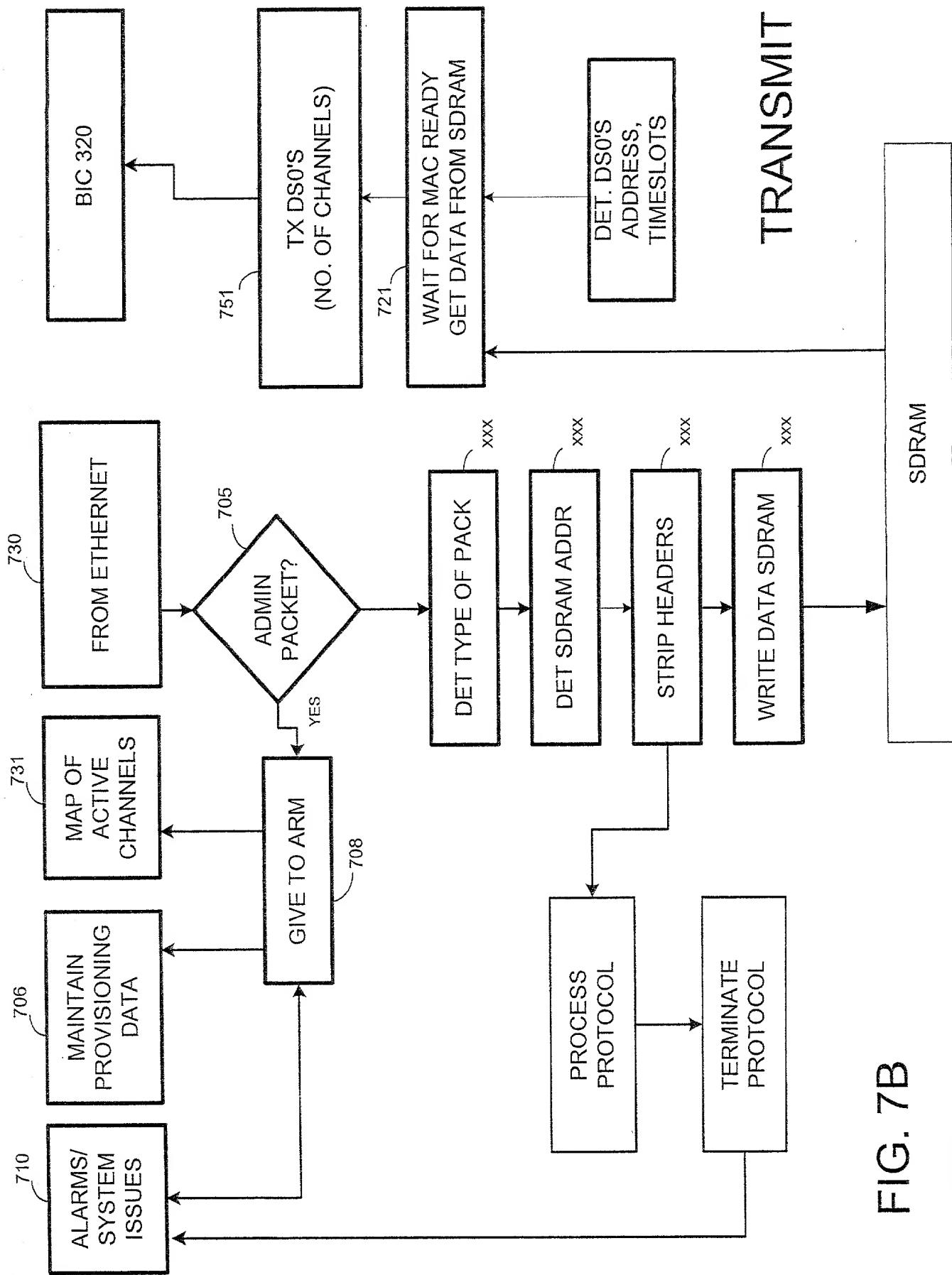


FIG. 7B

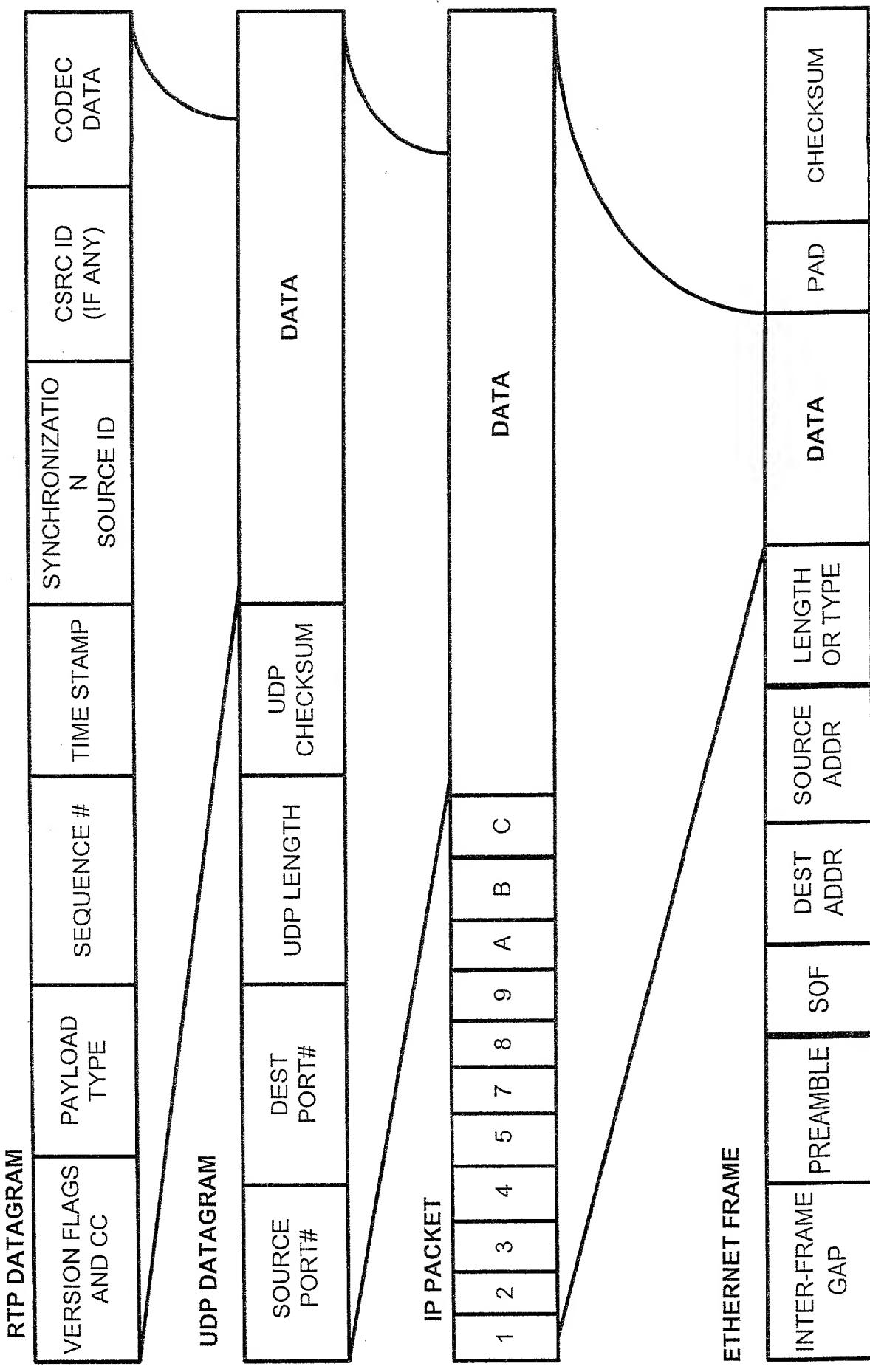


Figure 8

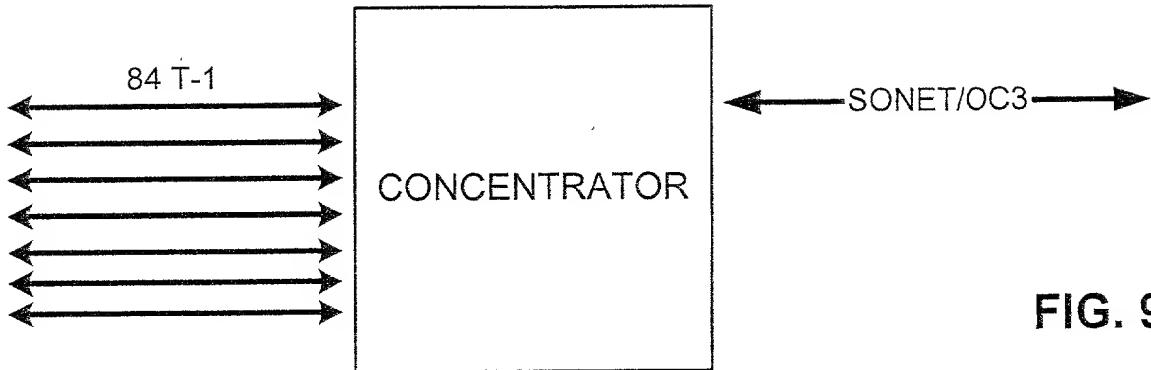


FIG. 9A

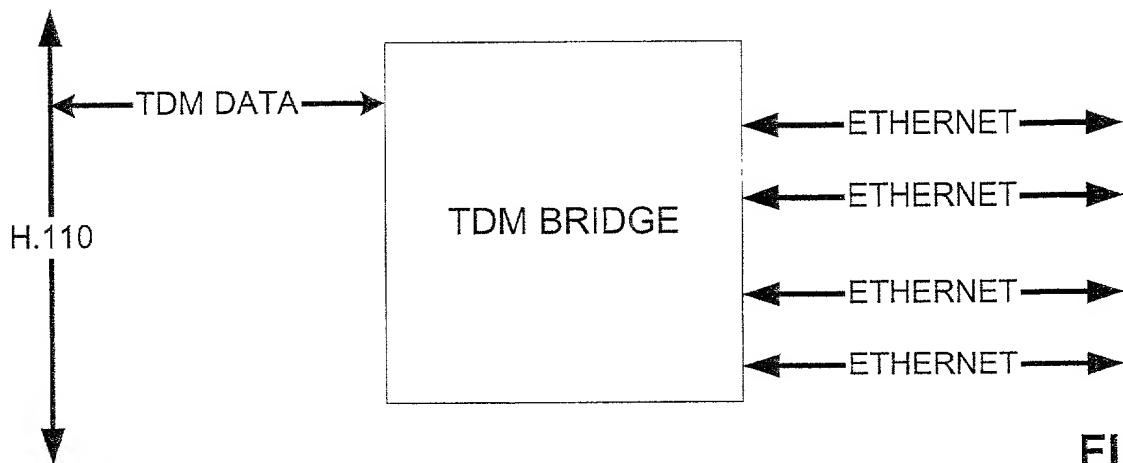


FIG. 9B

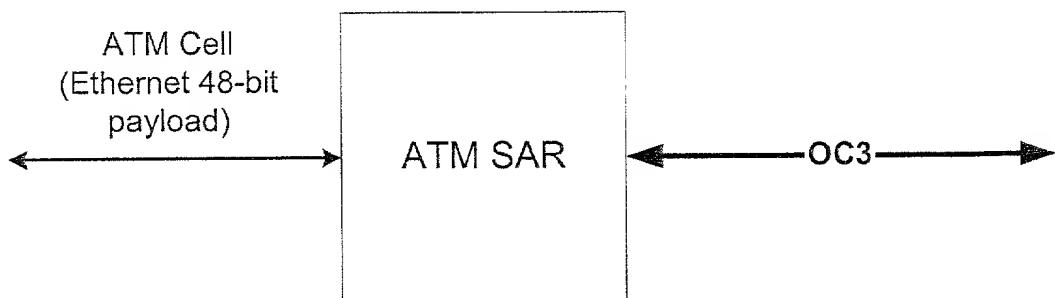


FIG. 10A

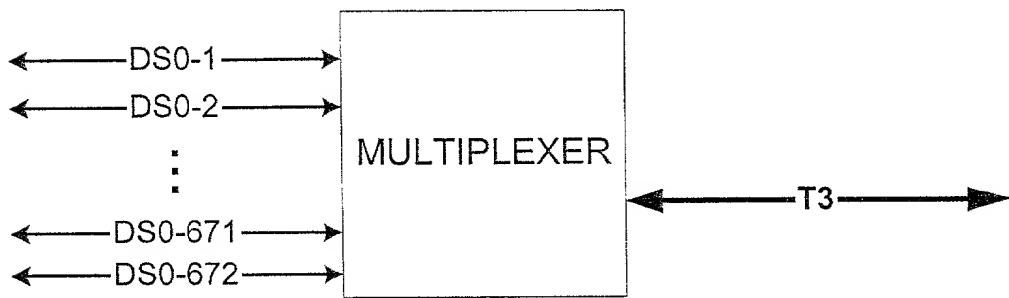


FIG. 10B

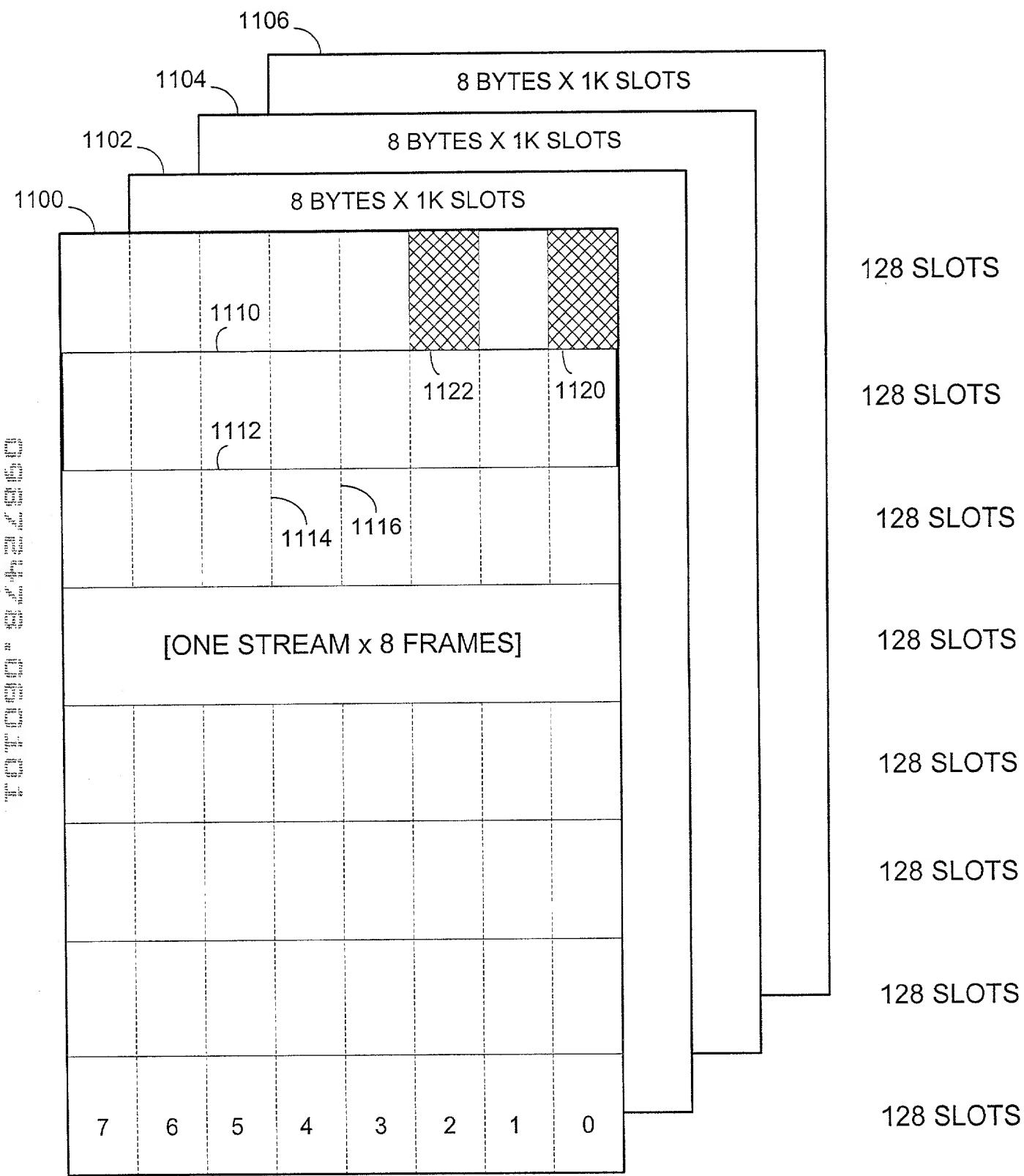


Figure 11

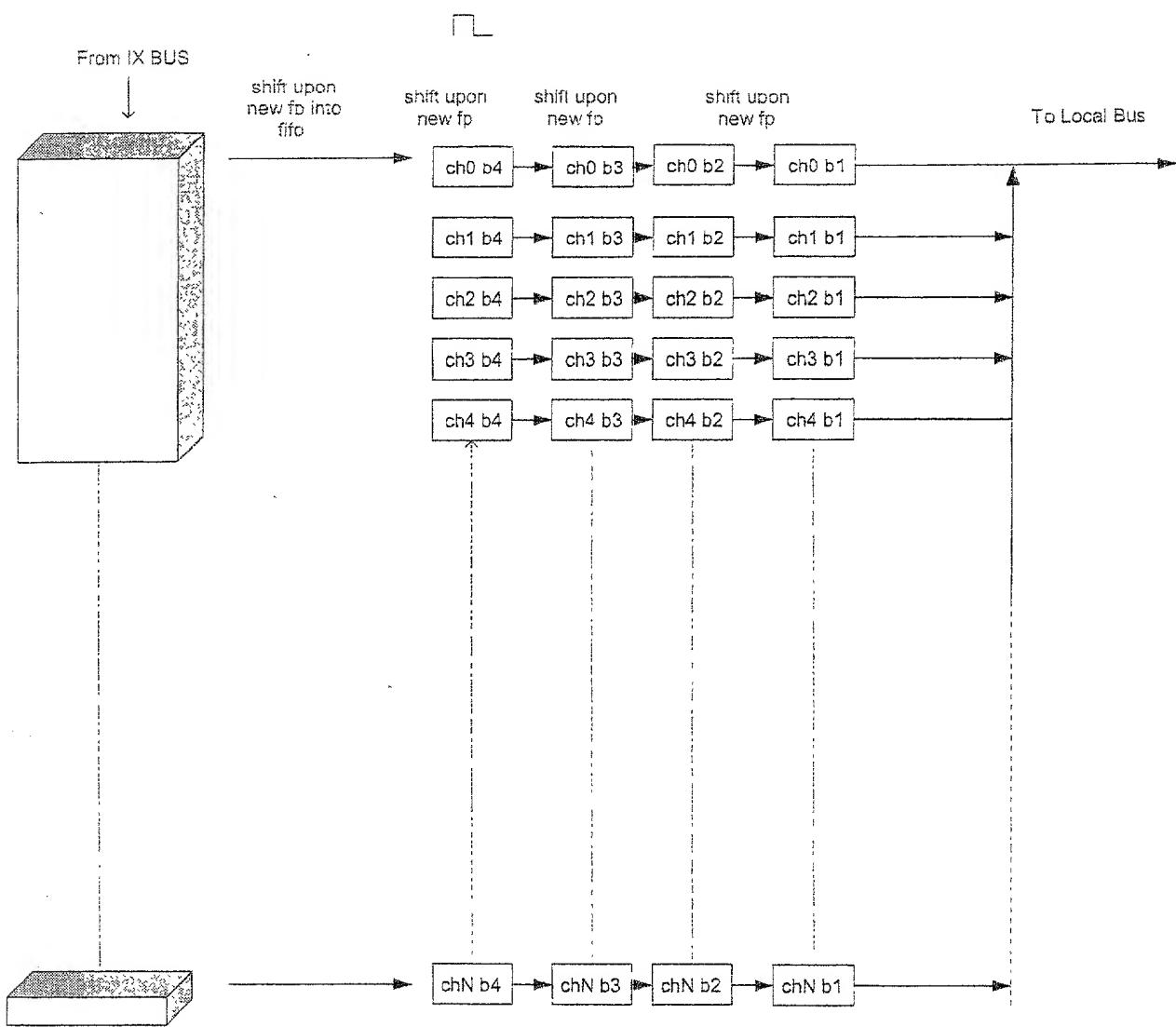
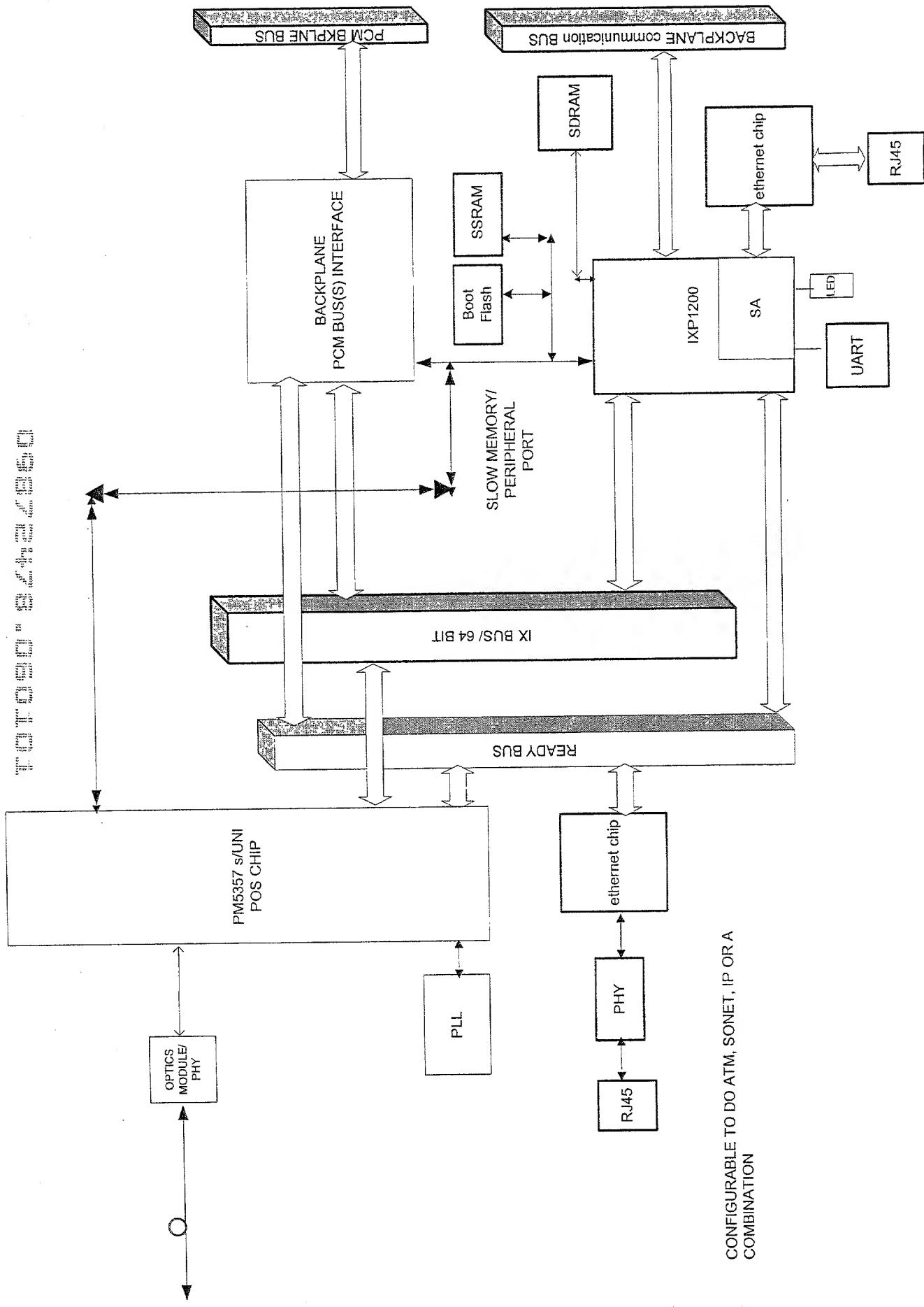


Figure 12

Figure 13



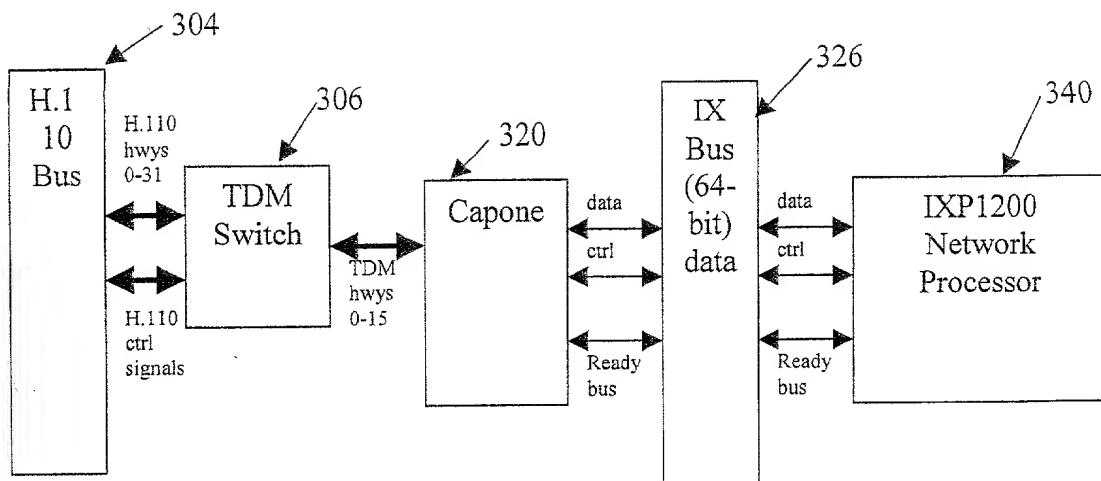


Figure 15

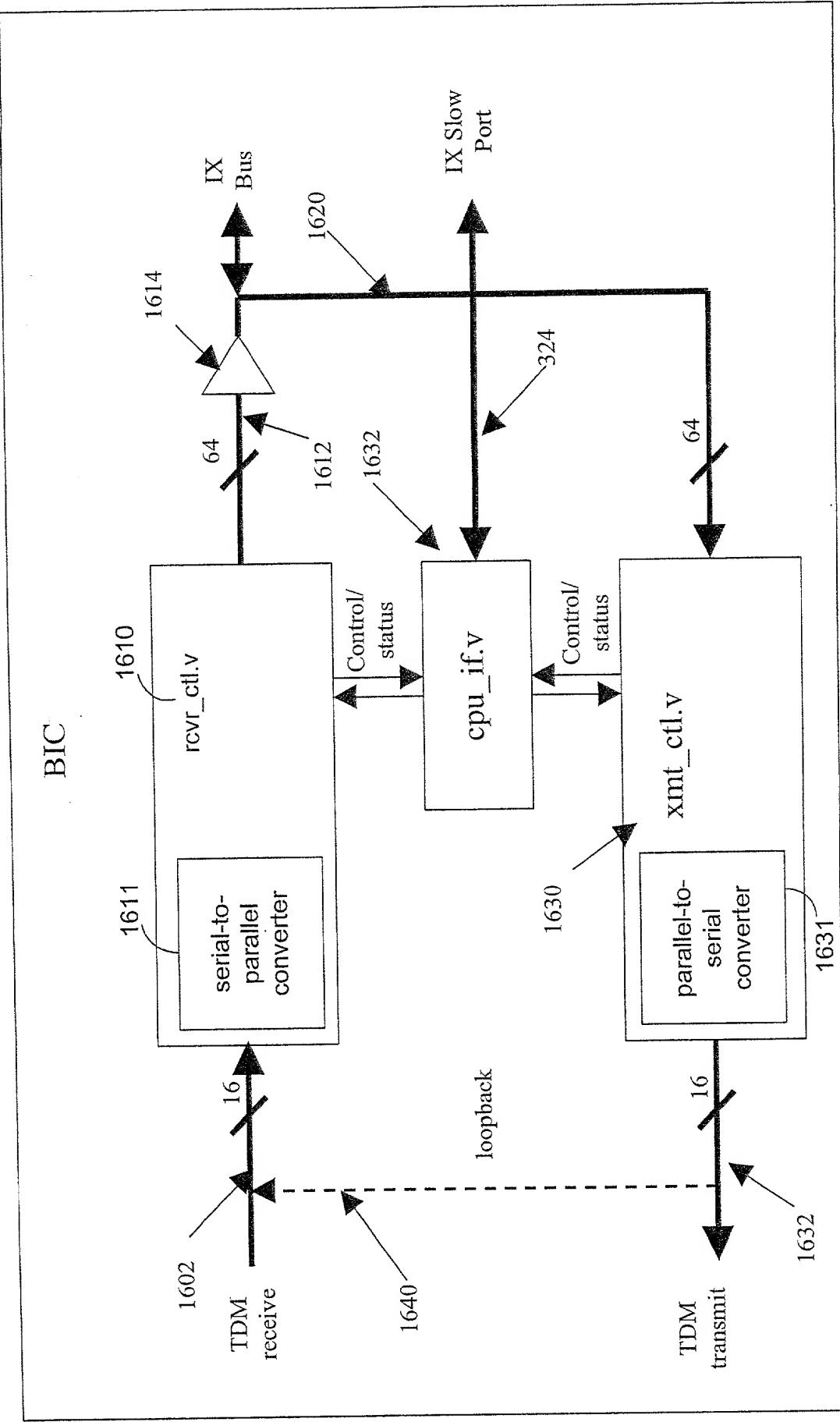


Figure 16A

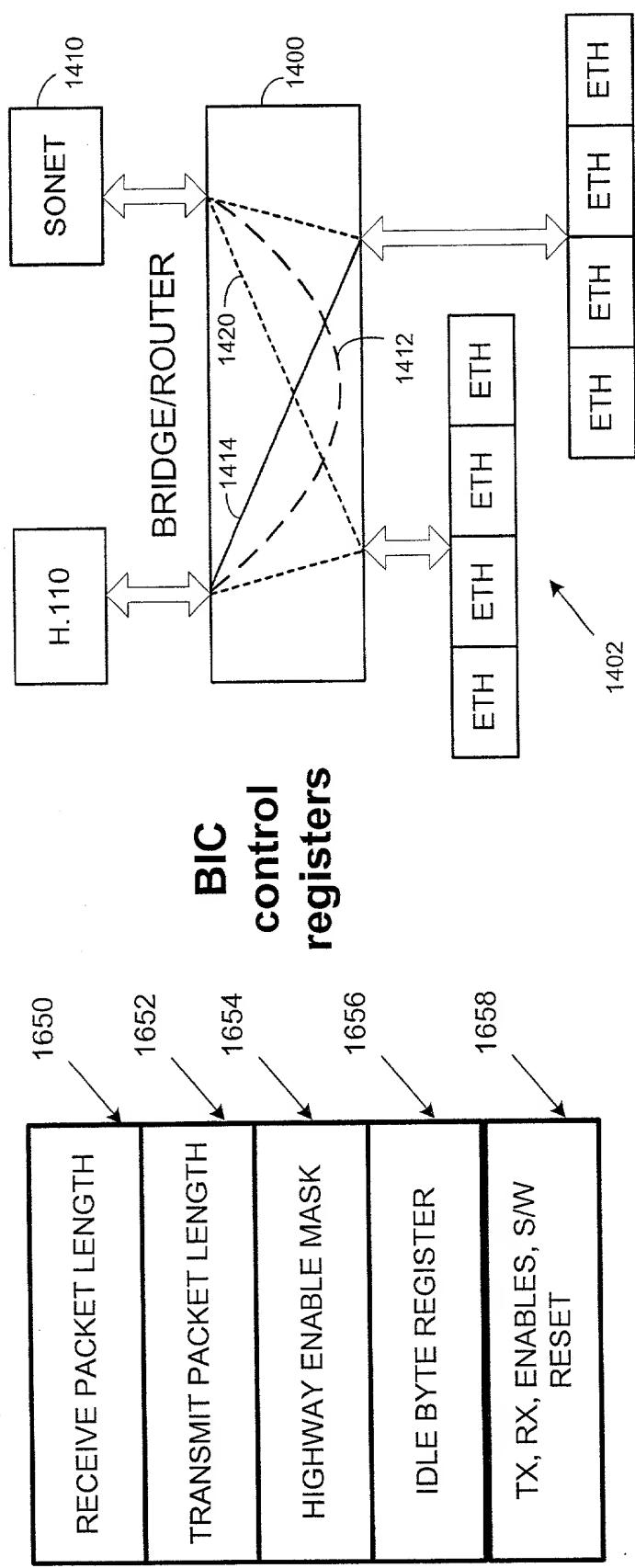


Figure 14

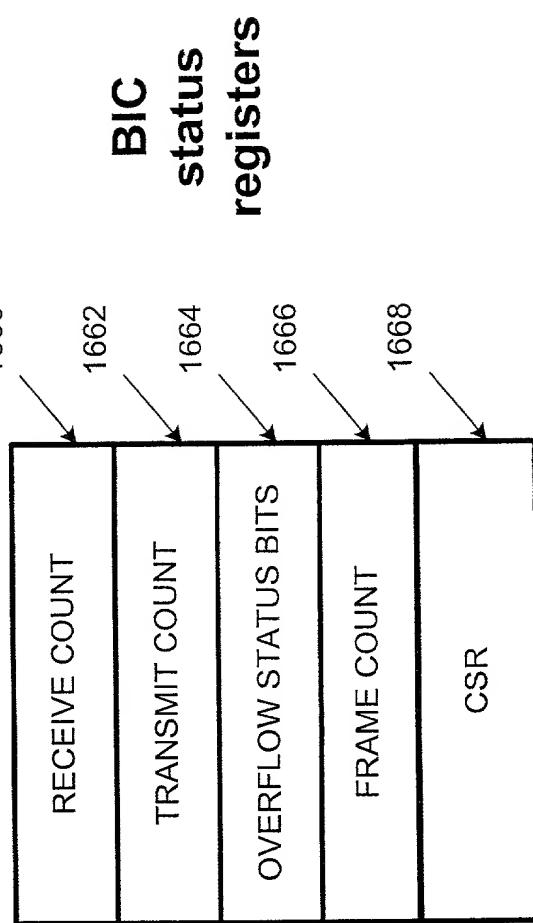


Figure 16B

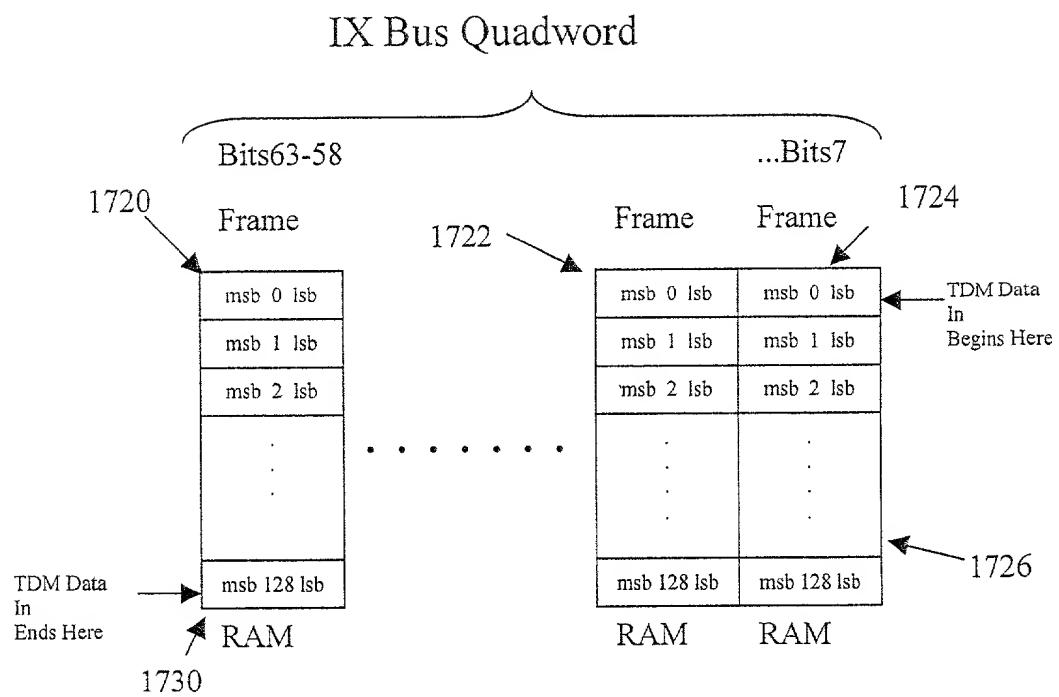


Figure 17

RAM8	RAM7	RAM6	RAM5	RAM4	RAM3	RAM2	RAM1	RAM0
Spare RAM Bank	Frame 7	Frame 7	Frame 6	Frame 5	Frame 4	Frame 3	Frame 2	Frame 1
	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0

Figure 18

IX Bus Quadword

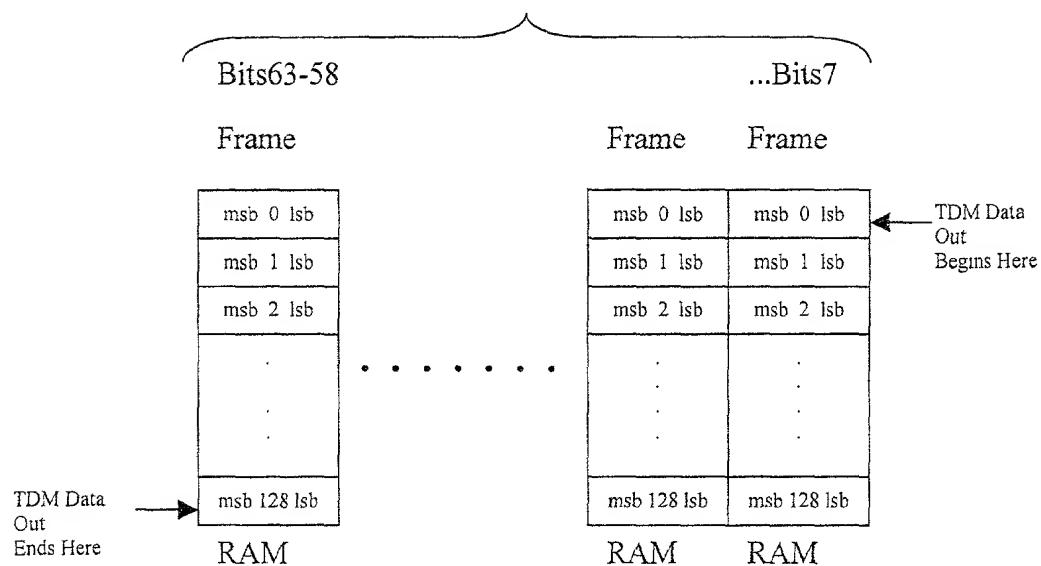


Figure 19

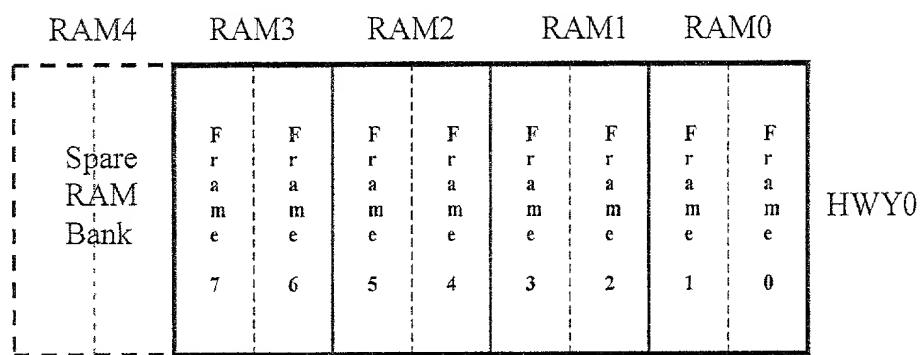


Figure 20